

The Axopatch 200B is the quietest patch clamp in the world.
It is even quieter than its predecessor, the Axopatch 200A.

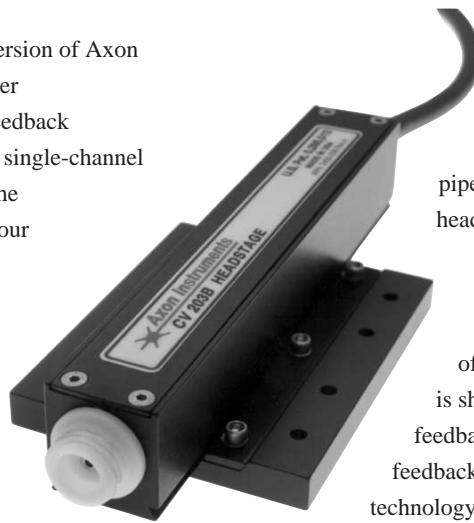
Axopatch 200B CAPACITOR-FEEDBACK PATCH CLAMP AMPLIFIER

200B单探头超低噪声膜片钳放大器。



The Axopatch 200B is the latest version of Axon Instruments' premier patch-clamp amplifier incorporating the innovative Capacitor-Feedback technology that provides the lowest-noise single-channel recording available. We have improved the integrating patch clamp in the 200B with our proprietary technology, which includes cooling of the active elements in the amplifier to achieve the lowest possible electrical noise. No previous amplifier has offered such low noise. Positioning your electrode will also be easier with our new, slim headstage design that improves access to the preparation.

Other enhancements include three recording configurations in a single headstage (one patch and two whole cell ranges, with capacitance compensation ranges of 100 pF and 1000 pF), increased voltage and current command ranges for electrochemical measurements, built-in capacitance dithering capability for capacitance measurements, and addition of series resistance compensation to the current clamp circuitry to improve performance. Seal Test now provides current steps in current clamp mode as well as voltage steps in voltage clamp mode. Leak Subtraction is now more sensitive in the most important resistance range. The new, slim headstage design improves electrode access to the preparation, and the recording bandwidth has been doubled to up to 100 kHz.



The amazingly low open-circuit noise of 0.13 pA rms (10 kHz) increases to only 0.145 pA rms when a patch-pipette holder is attached to the headstage input and the pipette capacitance is fully compensated (to eliminate capacitance charging transients). The power of capacitor-feedback technology is shown in Figure 1: capacitor feedback is clearly superior to resistive feedback; cooled capacitor-feedback technology is even better!

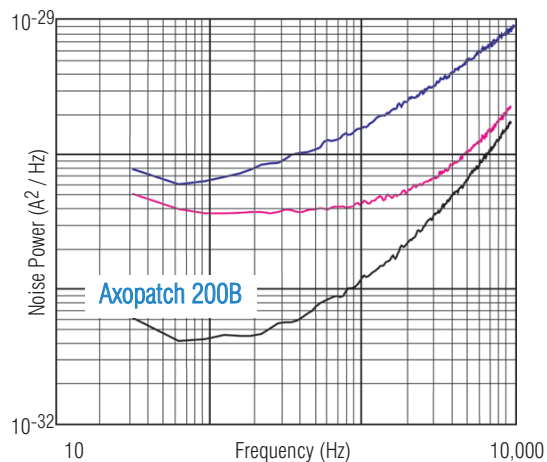
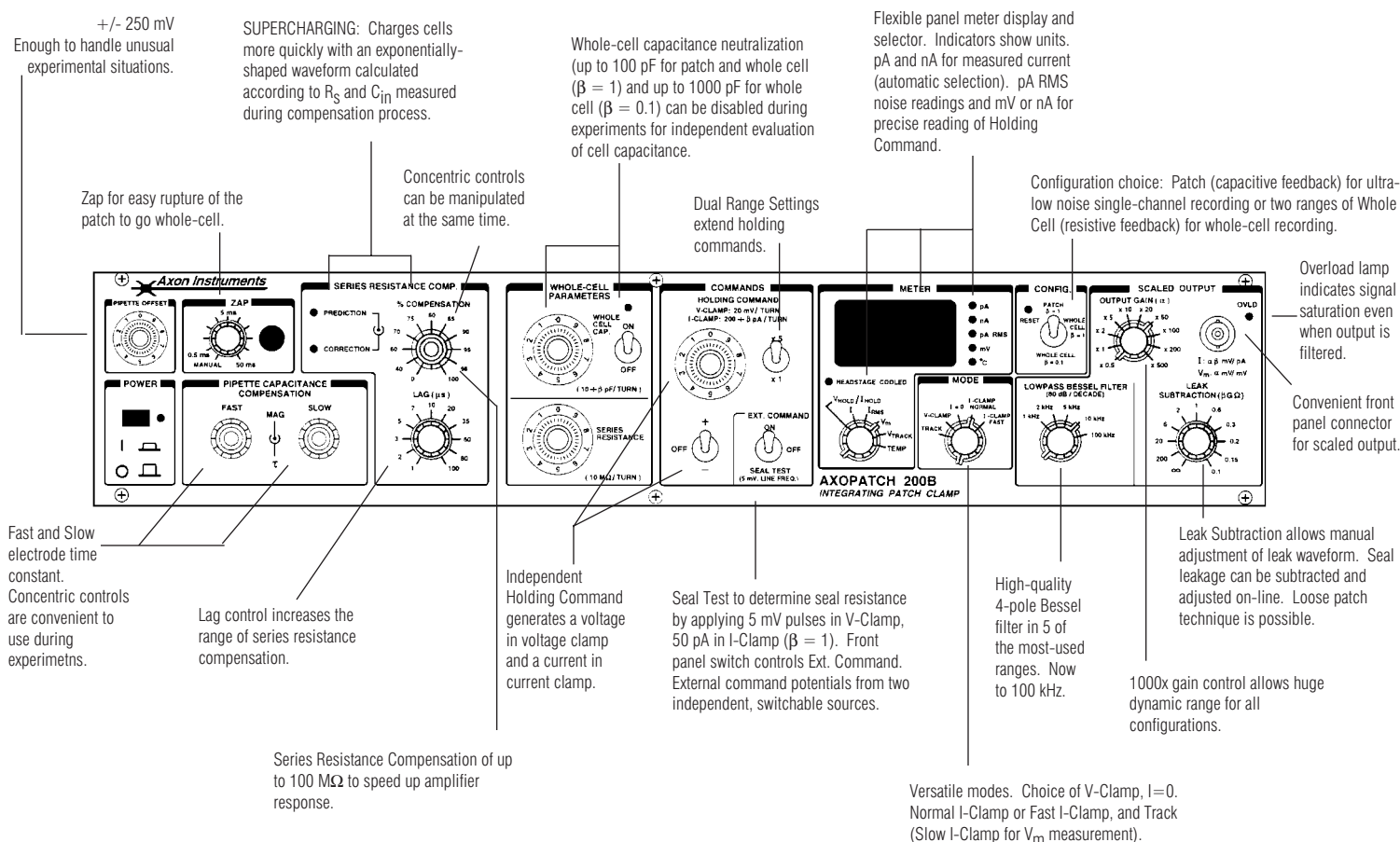


Figure 1. Noise Spectra of Patch-Clamp Amplifiers. The introduction of capacitor-feedback technology significantly reduced amplifier noise (Upper trace: resistive feedback; middle trace: capacitor feedback). With active cooling, the Axopatch 200B dramatically reduces noise to the lowest levels ever, over the entire bandwidth range (bottom trace: Axopatch 200B).

马普科学仪器有限公司

Tel: 020-8767 9617, 8767 9631; Fax: 020-8767 9635
<http://www.mapusci.com> E-mail: info@mapusci.com

CAPACITOR - FEEDBACK INTEGRATING PATCH CLAMP



Like its predecessors, the Axopatch 200B incorporates capacitive and resistive feedback elements in its headstage, providing the best possible performance for single-channel and whole-cell patch clamping. Convenient features include: **ZAP** (to rupture patches when going whole cell), **dual-speed current clamp** (to allow faster current clamping in small cells), **Holding Command** to set voltage commands in voltage clamp and current commands in current clamp, and a choice of three gain settings on the dedicated current output (for patch, whole-cell and loose-patch modes).

Unparalleled Performance, Utility and Ease of Use

Included: Efficient controls for whole-cell capacitance compensation, a unique "super-charging" form of series-resistance compensation that complements the conventional "correction" form, and a variable LAG control; output gain with 1000-fold dynamic range; 4-pole Bessel filter; built-in leak subtraction; dual external-command inputs; versatile panel meter

displaying Holding Command, rms current noise, membrane potential, tracking potential, current at the headstage input, and cooled circuitry temperature; telegraphic output of values for output gain, filter frequency, headstage mode (gain) and measured cell capacitance; ZAP; and dual-speed current clamp. Also, with the Axopatch 200B the bath is grounded for convenience of use and straightforward addition of command and compensation potentials.

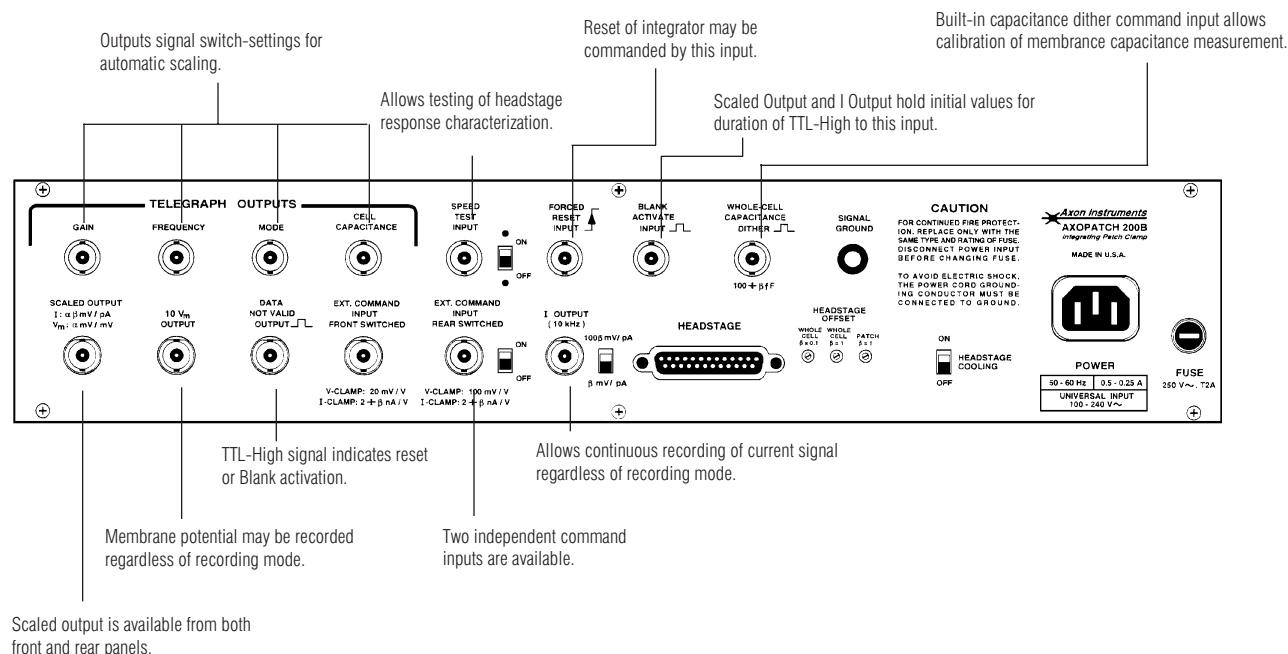
Integrating Headstage Mode — Quiet Single-Channel Recording

With its unprecedented low noise, the Axopatch 200B capacitor-feedback integrating headstage is ideal for measuring sub-picoamp current signals. The capacitor-feedback circuitry measures current as the rate of voltage increase across the capacitor; the voltage across the capacitor is the integral of the current. In contrast, resistor-feedback circuitry measures current as a voltage drop across a feedback resistor. This has the drawback

马普科学仪器有限公司

Tel: 020-8767 9617, 87679631; Fax: 020-8767 9635
http://www.mapusci.com E-mail: info@mapusci.com

Axopatch 200B



of requiring extremely high-value resistors that become less ideal as they become larger in value, with accompanying increases in their inherent noise. Noiseless capacitors, on the other hand, are easily manufactured. Thus, capacitor-feedback technology offers the lowest possible noise for single-channel recording.

Through the use of this technology, the electronic circuit noise has been reduced to such a level that the noise inherent in the components themselves has become significant. The Axopatch 200B actively cools the critical circuit components to about -15°C in order to reduce the thermal noise and so lowers the noise contribution of the amplifier itself to the lowest levels possible. The Axopatch 200B is the ultimate low-noise amplifier.

In the design of the integrating patch clamp we addressed two additional technological challenges that are encountered in the pursuit of ultra-low noise. First and foremost, the voltage across the capacitor cannot ramp forever in one direction. Eventually, the supply limits are reached and the electronics must reset to the starting point to continue ramping. During this reset, the system freezes the output at the last measured value. For

the best performance, this reset must be kept brief and must occur as infrequently as possible. The reset time of the Axopatch 200B is so short (50 ms) that users may never see a reset under most normal recording conditions. Second, to minimize the frequency of resets, the voltage range across the capacitor must be as large as possible. By using unique compensation circuitry, the Axopatch 200B manages to use the entire $\pm 10\text{ V}$ range of the power supply to charge the capacitor. This reduces the frequency of resets without incurring the penalty of longer reset times.

Bilayers

Headstages useful in artificial bilayer experiments must be stable with large input-capacitance loads. The Axopatch 200B is rock solid with an input capacitance of 1000 pF. Furthermore, the integrating technology in the Axopatch 200B enables the bilayer voltage to be stepped very quickly. There are two reasons for this. First, the integrator has an inherently large dynamic range that allows it to transiently pass currents of several microamperes. Second, during large command voltage steps the integrator enters the reset state. In this state, the headstage continues to operate as a voltage-clamp

马普科学仪器有限公司

Tel: 020-8767 9617, 87679631; Fax: 020-8767 9635
http://www.mapusci.com E-mail: info@mapusci.com

C A P A C I T O R - F E E D B A C K I N T E G R A T I N G P A T C H C L A M P

amplifier but with a very low feedback resistance. For the few milliseconds that the integrator is in the reset state it can pass up to 1 mA to rapidly charge the bilayer capacitance.

Resistive Headstage Mode — Superb Whole-Cell Performance

In whole-cell recording more current noise is produced by the cell and the environment than by the patch clamp amplifier. Hence, the benefits of a low-noise capacitor-feedback headstage cannot be effectively utilized in whole-cell mode. For this reason, the Axopatch 200B uses traditional resistor feedback headstage electronics for the whole-cell mode of patch clamp recording. The CV 203BU headstage includes two feedback resistors to provide a wide range of current-passing capacity in the whole-cell mode. The 500 M Ω feedback resistor ($\beta = 1$) provides both low noise and a large current passing ability (20 nA). For larger currents, one can switch to the 50 M Ω feedback-resistor ($\beta = 0.1$) to pass up to 200 nA.

Series Resistance Prediction

There are two goals of series resistance compensation. The first is to step the membrane potential to an assigned value as rapidly as possible. One way to speed up this process is to transiently increase the size of the command voltage step (applying a method known as “supercharging”). This causes the charging curve to briefly assume the steeper rate appropriate for a larger step. This transient, high-charging rate is terminated when the membrane potential approaches the intended value. On the Axopatch 200B, supercharging is available through the **Series Resistance PREDICTION** control. The supercharging transient in the Axopatch 200B is automatically and conveniently determined by the user when the conventional whole-cell capacitance controls are set. Prediction refers to our unique algorithm for calculating the added supercharging waveform. Note that this technique does not require the command to be a voltage step; other commands, such as a sine wave, will also be supercharged.

Series Resistance Correction

The second goal of series resistance compensation is to

measure the ionic currents. To eliminate error due to the voltage drop across the electrode and to improve the bandwidth of the recording, conventional **Series Resistance CORRECTION** is required. This technique employs positive feedback to increase the command potential by adding a signal proportional to the measured current to the command input of the headstage.

Series Resistance Prediction and Correction may be used together or separately on the Axopatch 200B. The controls are concentric and can be manipulated at the same time. Turning the controls together is quite similar to the series resistance percentage compensation found on other patch clamps. In experiments with real cells we have found that both the Series Resistance Prediction and Correction on the Axopatch 200B can be routinely set to levels exceeding 90%, enabling fast and accurate whole-cell voltage clamping. A variable LAG control permits an even greater degree of series resistance compensation to be achieved, although not at the highest frequencies.

Dual-Speed Current Clamp

The Axopatch 200B employs a dual-speed current clamp to optimize speed and stability. The choice of speeds depends on pipette resistance. For pipette resistances above 10 M Ω , the I-Clamp **Fast** mode can be employed to obtain a rise time of 10 μ s under many experimental conditions. The I-Clamp **Normal** mode guarantees stability for any pipette resistance above 1 M Ω . In addition, current clamp to zero current ($I=0$ mode) is available, as is a slower clamp to zero current (Track mode) useful for following slow changes in pipette offset while approaching a cell before seal formation. The Holding Command can be used to set a holding current in I-Clamp. Two external command inputs are provided to permit multiple command sources (for cell capacitance experiments, current clamp, etc.). In addition, the Axopatch 200B includes series resistance compensation in current clamp that allows for correction of voltage errors due to the pipette resistance.

Pipette Offset

Researchers are putting ever more unusual solutions into patch pipettes, producing large offset potentials. The Axopatch 200B provides ± 250 mV of offset potential to handle even the most adverse situations.

马普科学仪器有限公司

Tel: 020-8767 9617, 87679631; Fax: 020-8767 9635
<http://www.mapusci.com> E-mail: info@mapusci.com

Dual Command Potentials

Two separate command potential inputs allow you to sum command input signals from two different sources. For instance, you may wish to constantly supply a low-level sine wave command for evaluation of membrane capacitance. Large voltage steps may be mixed with this signal to construct a C_m vs. V_m relationship. One of these command potentials may be switched on from the front panel to access the external source. The other is switched separately on the back panel. The back panel command is scaled to afford greater range (up to ± 1 V), and so is quite useful for electrochemical measurements.

Seal Test

A convenient seal test is built into the External Command potential front panel switch. The oscillator frequency is set to the line frequency for automatic synchronous triggering on any oscilloscope. Seal Test may be used in voltage clamp mode (5 mV pulse) or in current clamp mode (50 pA ($\beta = 1$) or 500 pA ($\beta = 0.1$)) pulses.

Holding Command

The Holding Potential potentiometer can be used to manually establish the cell holding potential for voltage clamp or a holding current for current clamp. The Holding Command of the Axopatch 200B has been enhanced over that of its predecessors with the addition of a x1 and x5 switch that allows you to choose either 0-200 mV or 0-1 V ranges. (In current clamp, the command response at $\beta = 1$ is 0-2 nA or 0-10 nA, and at $\beta = 0.1$ it is 0-20 nA or 0-100 nA.) An ON/OFF switch can disable this control when an external command from a computer is used to establish the holding potential.

Pipette Capacitance Compensation

Pipette Capacitance Compensation is in operation in both voltage- or current-clamp modes. There are controls to establish the Magnitude and τ of two time constants, Fast and Slow, allowing complete compensation of the pipette capacitance waveform. Magnitude and τ are controlled by concentric knobs allowing easy and convenient adjustment of parameters for each time constant.

Cell Capacitance Compensation

The Whole-Cell Capacitance control can compensate up to 100 pF ($\beta = 1$) or 1000 pF ($\beta = 0.1$) to allow recording from a large range of cell sizes. The control may be disabled by an ON/OFF switch to allow evaluation of the cell capacitance charging waveform at any time during an experiment.

Capacitance Dithering

Useful for cell membrane capacitance measurements, Capacitance Dithering may be enabled during a TTL-High level signal to Whole Cell Capacitance Dither input to effectively increase the observed cell capacitance by 100 fF ($\beta = 1$) or 1 pF ($\beta = 0.1$). It may be used in conjunction with the DR-1 Resistance Dither unit (supplied with the Axopatch 200B) which normally provides a short-circuit link between preparation and ground. The DR-1 inserts a 500 k Ω resistor in series with bath ground during a TTL-High signal and is suitable for finding the phase tracking angle in capacitance measurement experiments.

Output Gain

Ten gain settings spanning a 1000-fold range may be selected to scale the output to the most desirable level. The Axopatch 200B output swing is ± 10 V, providing the greatest compatibility to the vast range of recording devices. However, the dynamic range of the instrument at unity gain is greater than the output swing. Thus a gain value of 0.5 is available to take advantage of the full dynamic range of the Axopatch 200B headstage. (Note: The full range is also faithfully reported by the front panel meter.)

Low-Pass Filters

The scaled output passes through a 4-pole Bessel filter. The five settings from 1 kHz up to 100 kHz allow you to set the band pass appropriate to your application over a range double that previously available.

Panel Meter

The panel meter can report values for current and voltage while in any operational mode. Steady-state current is reported to monitor voltage-clamp output. To evaluate noise, current is displayed as an rms value. The

V_m setting is used under current clamp to evaluate

CAPACITOR - FEEDBACK INTEGRATING PATCH CLAMP

membrane potential. In I=0 mode, V_m is the resting membrane potential of the cell. V_{HOLD}/I_{HOLD} reports the setting of the Holding Command. The value is reported even when this command is switched off, allowing you to precisely set a desired holding command in advance. The temperature of the cooled headstage circuitry may be displayed when TEMP is selected.

Leak Subtraction

It is often convenient to subtract leak errors during an experiment. The Axopatch 200B allows you to correct leak errors as they appear without adjusting your data-acquisition software settings. This is also useful when a large seal leakage is expected, as in loose-patch experiments. Leak Subtraction has been improved in the Axopatch 200B by increasing sensitivity in the resistance range most likely to be encountered by researchers.

Zap

To go whole cell it is necessary to rupture the cell-attached patch. As an alternative to carefully controlled suction, the Axopatch 200B allows the patch to be ruptured by applying a single +1.3 V pulse for a chosen duration.

Improved Headstage Design

The newly engineered headstage has only one third the cross-sectional area of previous headstages, thereby greatly improving ease of access to the preparation.

Excellent Manual

Written by scientific consultants, with the assistance of Axon Instruments staff, the Axopatch 200B manual will serve as a useful guide to the operation of the Axopatch 200B as well as an informative reference for many aspects of patch clamping.

Pipette Holders

The pipette holder, HL-U, is custom constructed to ensure low-noise mechanically stable recording. Two different barrel lengths are provided. HL-U accepts pipettes of 1.0 mm to 1.7 mm OD. It includes a silver wire assembly, and has a 1.0 mm OD post for suction. The Axopatch 200B is shipped with one pipette holder. For further information see HL-U Holder in the

Headstages and Holders section of this catalog.

Specifications

Unless otherwise noted: $T_A = 20^\circ\text{C}$, 1 hr warm up time.

CV 203BU HEADSTAGE

Construction: All critical components are in a sealed hybrid and cooled with a solid state cooling element.
Configuration: High-speed, low-noise current-to-voltage converter.
Cooling: Input circuitry -15°C typical. Headstage cooling should be kept on at all times to ensure proper calibration of offset voltages.

Headstage Gain (β): 1 mV/pA ($\beta = 1$) Patch or Whole-Cell modes.
 0.1 mV/pA ($\beta = 0.1$) Whole-Cell mode.

Feedback Element: Patch 1 pF
 Whole Cell $\beta = 1$, 500 M Ω in parallel with 1 pF.
 Whole Cell $\beta = 0.1$, 50 M Ω in parallel with 1 pF.

Feedback Element Selection: FET switches in hybrid enable remote selection of either a capacitor (Patch mode) or a parallel combination of a capacitor and resistor (Whole-Cell mode).

Tuning (Whole Cell mode only): Tuning circuit to idealize response of the feedback resistor is contained in the main instrument. Tuning is automatically bypassed when the capacitive feedback is selected.

Pipette-Capacitance-Compensation Injection Capacitor: 1 pF

Whole-Cell-Capacitance-Compensation Injection Capacitor:

Patch mode: none
 Whole Cell mode: CV 203BU: 5 pF, $\beta = 1$
 50 pF, $\beta = 0.1$

Case: Case connected to ground. Case jack mates to 1 mm plugs.

Bandwidth: Test signal applied via Speed Test input; Patch or Whole Cell mode:

Internal: 140 kHz capacitor feedback
 70 kHz resistor feedback
 Max. External: 100 kHz (limited to output filter)

Capacitive Load Stability: 1000 pF, 0 Ω in series

Maximum Instrument Noise: Measured with minimal external noise sources (*i.e.*, radiated line-frequency noise, mechanical vibration) 8-pole Bessel filter.

		PATCH $\beta = 1$	WHOLE CELL $\beta = 1$	WHOLE CELL $\beta = 0.1$
Without holder:	line frequency & harmonics	0.005 pA _{p-p}	0.005 pA _{p-p}	0.005 pA _{p-p}
	0.1-100 Hz	0.020 pA _{p-p}	0.50 pA _{p-p}	1.6 pA _{p-p}
	0.1-1 kHz	0.015 pA _{rms}	0.25 pA _{rms}	0.75 pA _{rms}
	0.1-5 kHz	0.060 pA _{rms}	0.55 pA _{rms}	1.65 pA _{rms}
	0.1-10 kHz	0.130 pA _{rms}	1.10 pA _{rms}	3.0 pA _{rms}
With holder:	0.1-10 kHz	0.145 pA _{rms}	1.10 pA _{rms}	3.0 pA _{rms}

RESET CHARACTERISTICS (Patch Mode only)

Total reset time: 50 $\mu\text{s} \pm 10\%$
 this includes: integrator reset 10 μs
 differentiator reset 30 μs
 other overhead 10 μs

Time between resets (T_{BR}):

For DC currents: $T_{BR} = 10/(I_{DC} - I_{BIAS})$

where I_{DC} and I_{BIAS} are in pA and T_{BR} is in seconds.

I_{BIAS} is typically 10-30 fA.

For transient currents: A reset will occur if the headstage must deliver more than 10 pC of charge to the membrane. For example, a 60 mV step imposed on a 200 pF bilayer membrane will cause a reset (12 pC of charge needed) whereas a 40 mV step will not (8 pC of charge needed).

马普科学仪器有限公司

Tel: 020-8767 9617, 87679631; Fax: 020-8767 9635
<http://www.mapusci.com> E-mail: info@mapusci.com

Axopatch 200B

Reset transients in current waveform at Scaled Output (typical)

100 Hz	bandwidth	± 0.25 pA
1 kHz		± 0.5 pA
10 kHz		± 2 pA

CURRENT CLAMP

The current clamp mode has two speed settings: I-Clamp Normal and I-Clamp Fast. I-Clamp Normal is for use with electrode resistances greater than 1 M Ω . I-Clamp Fast is for use with electrode resistances greater than 10 M Ω .

The speed in I=0 mode is the same as in I-Clamp Normal. In addition, Track mode is a slow clamp to zero current. Note that series resistance compensation remains active in current clamp mode, allowing measurement of pipette resistance and (when R_s is compensated) accurate monitoring of cell membrane potential, but the speed setting is still determined by the actual electrode resistance and not only the remaining uncompensated resistance.

The speed of the current clamp depends on the Mode setting (Normal or Fast), the time constant of the cell and the pipette resistance.

R_p	R_m	C_m	10-90% rise time (overshoot) I-Clamp Normal	10-90% rise time (overshoot) I-Clamp Fast
1 M Ω	0 M Ω	0 pF	15 μ s (10%)	N/A
1 M Ω	500 M Ω	33 pF	350 μ s (0%)	N/A
10 M Ω	0 M Ω	0 pF	200 μ s (20%)	20 μ s (<1%)
10 M Ω	500 M Ω	33 pF	250 μ s (10%)	10 μ s (<1%)
50 M Ω	500 M Ω	33 pF	500 μ s (30%)	150 μ s (<1%)

CAPACITANCE COMPENSATION

Pipette Capacitance

Fast τ:	0.2-2 μ s	Slow τ:	0.05-10 ms
Fast Magnitude:	0-10 pF	Slow Magnitude:	0-1 pF

These controls are used to charge pipette capacitance. In I-Clamp modes they act as a negative capacitance.

	$\beta = 1$	$\beta = 0.1$
Whole-Cell Capacitance:	0.3-100 pF	3-1000 pF
Series Resistance:	0-100 M Ω	0-100 M Ω

These controls are used to charge membrane capacitance in whole-cell V-Clamp. For Patch mode, whole-cell capacitance is not operative. In I-Clamp modes only the Series Resistance control is operative. The whole-cell capacitance control places an analog voltage proportional to setting on Cell Capacitance Telegraph Output.

SERIES RESISTANCE COMPENSATION

% Prediction: OFF, 0-100%. Acts with Whole-Cell Parameters to speed up charging of the membrane. Maximum achievable % Prediction is limited by the magnitude of the voltage step.

% Correction: OFF, 0-100%. Acts with Series Resistance setting to reduce series resistance errors and to speed up response to ionic currents.

Lag: 1-100 μ s. Cuts high-frequency response of series-resistance correction circuit to enable a higher Correction setting.

CAPACITANCE DITHERING

Enabled during a TTL High level signal to Whole Cell Capacitance Dither input.

Effectively increases the observed cell capacitance by 100 fF ($\beta = 1$) or 1 pF ($\beta = 0.1$). Useful for cell membrane capacitance measurements. May be used in conjunction with the DR-1 Resistance Dither unit.

DR-1 Resistance Dither unit (supplied with the Axopatch 200B) normally provides a short-circuit link between preparation and ground. Inserts a 500 k Ω resistor in series with bath ground during TTL High signal. Suitable for finding the phase tracking angle in capacitance measurement experiments.

MODE

V-Clamp: Pipette voltage is clamped.

I-Clamp Normal or Fast:

Pipette current is clamped to command current from Holding Command knob or external input. **Normal** mode is stable for electrode resistances greater than 1 M Ω . **Fast** mode is stable for electrode resistances greater than 10 M Ω . Series Resistance control is active.

Track: Slow I-Clamp to zero current.

(I=0): I-Clamp to zero current used to correct pipette offset.

Selected mode sets analog voltage on Mode Telegraph Output.

COMMAND POTENTIALS

Seal Test: 5 mV (V-Clamp mode), 50 pA (I-Clamp, $\beta = 1$) or 500 pA (I-Clamp, $\beta = 0.1$) command at line frequency.

External

Commands: Two separate BNC inputs, one front-switched, one rear-switched. Sensitivity: Front-Switched: 20 mV/V in V-Clamp, $2+\beta$ nA/V in I-Clamp, disabled in TRACK and I=0. Rear-Switched: 100 mV/V in V-Clamp, $2+\beta$ nA/V in I-Clamp, disabled in Track and I=0. Input impedance: 10 k Ω . Inputs may be connected in parallel to increase sensitivity.

Holding

Command: Ten-turn potentiometer with dial. Polarity switch. Value can be previewed on meter.
V-Clamp mode: ± 200 mV (toggle X1); ± 1 V (toggle X5)
I-Clamp modes: ± 2 nA for $\beta=1$, ± 20 nA for $\beta=0.1$ (toggle X1)
 ± 10 nA for $\beta=1$, ± 100 nA for $\beta=0.1$ (toggle X5)
Disabled in Track and I=0 modes.

Pipette Offset:

Manual: ± 250 mV. Ten-turn control with uncalibrated dial.

Track, I=0: ± 1 V. Nulling potential automatically adjusts to maintain zero pipette current.

ZAP

Amplitude: $+1.3 V_{DC}$ at pipette for chosen duration.

Duration: 0.5-50 ms or Manual. Triggered by front-panel pushbutton. In Manual position Zap amplitude is maintained as long as pushbutton is depressed.

RMS NOISE

3.5 digit meter displays rms current noise in pA. Measurement bandwidth is 30 Hz to 5 kHz. Upper -3 dB frequency is set by 4-pole Butterworth filter.

INPUTS

Forced Reset: Positive edge triggered. Initiates a reset of the integrator; has no control over the duration of reset.

Blank Activate: Causes Scaled Output and I Output to hold their initial value for the duration of the blanking pulse. Does not affect 10 V_m output.

Speed Test: Injects current into headstage input through a 1 pF capacitor. Injected current waveform is the derivative of the voltage waveform applied at Speed Test input. For example, a 100 Hz 10 V_{p-p} triangle wave will inject a 1 nA $_{p-p}$ square wave into the headstage input.

SIGNAL OUTPUTS

Scaled Output: Scaled and filtered by output control settings. Sample and hold pedestal compensation. Output is I ($\alpha\beta$ mV/pA) when in V-Clamp or Track modes. Output is V_m (α mV/mV) when in I-Clamp mode. BNCs on front and rear panels are identical.

I: Pipette current. Rear-panel switched gain of either β mV/pA or 100 β mV/pA; fixed filter: 10 kHz 3-pole Bessel. Output does not benefit from sample and hold pedestal compensation.

10 V_m : Membrane potential at x10 gain. Junction potentials removed.

¹ Applicable for $\beta = 0.1$ only.

² Applicable for $\beta = 1$ only.

CAPACITOR - FEEDBACK INTEGRATING PATCH CLAMP

OUTPUT CONTROLS

- Output Gain (α):** 10 values from 0.5-500. Affects Scaled Output only. Selected value sets analog voltage on Gain Telegraph Output for reading by computer.
- Lowpass Bessel Filter:** 4-pole lowpass Bessel filter with five settings; 1, 2, 5, 10 and 100 kHz. Selected value sets an analog voltage on Frequency Telegraph Output.
- Leak Subtraction:** Causes a signal proportional to the command to be subtracted from current record. Range: 100 β M Ω to 1000 β M Ω .

TELEGRAPH OUTPUTS

Gain: Takes α and β gain factors into account.

I (mV/pA)	0.05 ¹	0.1 ¹	0.2 ¹	0.5	1	2	5	10	20	50	100 ²	200 ²	500 ²
V _m (mV/mV)				0.5	1	2	5	10	20	50	100	200	500
Telegraph output (V)	0.5	1.0	1.5	2.0	2.5	3.0	3.5	4.0	4.5	5.0	5.5	6.0	6.5

Frequency:

Filter setting (kHz)	1	2	5	10	100
Telegraph output (V)	2	4	6	8	10

Mode:

	Track	V-Clamp	I=0	I-Clamp Normal	I-Clamp Fast
Scaled Output	I	I	V _m	V _m	V _m
Telegraph (volts):	4	6	3	2	1

Cell Capacitance:

Telegraph output: 0 to +10 V, proportional to setting 0-100 pF (for $\beta = 1$).
0 to -10, proportional to 0-1000 pF (for $\beta = 0.1$). 0 V when Whole Cell Cap switch is in the OFF position.

Data Not Valid

Output goes High during a reset in Patch mode or for the duration of a Blank Activate pulse in either Patch or Whole Cell mode.

PANEL METER

3.5 digit meter displays Track potential (V_{TRACK}) in mV, membrane potential (V_m) in mV, current noise (I_{RMS}) in pA rms, membrane current (I) in pA or nA, Holding Command (V_{HOLD}/I_{HOLD}) in mV or nA or input circuitry temperature in degrees Celsius (TEMP). Meter has autoranging feature for all settings except TEMP.

GROUNDING

Signal ground is isolated from chassis and power ground. Signal ground is available on rear panel.

CONTROL INPUTS

Above 3 V accepted as logic High. Below 2 V accepted as logic Low. Inputs protected to ± 15 V.

MODELS

Unit is supplied with two model assemblies, the PATCH-1U and the MCB-1U.

PATCH-1U Model Cell

Emulates three experimental conditions:

- BATH: 10 M Ω electrode resistor to ground. 4 pF pipette capacitance.
CELL: 10 M Ω electrode resistor connected to a 500 M Ω /33 pF cell.
4 pF pipette capacitance.
PATCH: 10 M Ω electrode connected to a 10 G Ω patch. 5 pF pipette capacitance.

MCB-1U Model Bilayer

Emulates a bilayer membrane. 10 K Ω resistor in series with a 100 pF capacitor.

HEADSTAGE AND CABINET DIMENSIONS

Case is 0.7 x 0.75 x 4.2" (17.8 x 19.0 x 105.0 mm). A removable mounting plate 2.5 x 2.0 x 0.25" (62 x 50 x 6.3 mm) is supplied. Cable length is 10 feet (3 m). Cabinet is 3.5" (89 mm) high, 19" (483 mm) wide, 12.5" (317 mm) deep. Mounts in standard 19" rack. Handles included. Net weight 11.5 lbs. (5.1 kg).

PIPETTE HOLDERS

HL-U holders mate to threaded Teflon input connector of the CV headstage. Post for suction tubing is 1 mm OD. HL-U holder accepts glass 1.0-1.7 mm OD. Supplied with silver wire. Optional HLR-U right-angle adapter and HLB-U BNC adapter are available.

SUPPLY REQUIREMENTS

- Line Voltage:** 85 to 264 V_{AC} (110 to 340 V_{DC}) Universal voltage input.
Line Frequency: 50-60 Hz.
Power: 30 W.
Fuse: 0.5 A slow.
5 x 20 mm.
Line Filter: RFI filter is included.
Line cord: Shielded line cord is provided.

ACCESSORIES PROVIDED

- Theory and Operation Manual
- One HL-U pipette holder
- Spare fuse
- PATCH-1U model cell
- MCB-1U model bilayer
- DR-1 series resistance dither unit

马普科学仪器有限公司

Tel: 020-8767 9617, 87679631; Fax: 020-8767 9635
http://www.mapusci.com E-mail: info@mapusci.com